



IFW

**PATENT APPLICATION**  
**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: Q76951

Koji ARITA, et al.

Appln. No.: 10/642,279

Group Art Unit: 2814

Confirmation No.: 2411

Examiner: Long PHAM

Filed: August 18, 2003

For: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING SAME

**RESPONSE TO RESTRICTION REQUIREMENT**

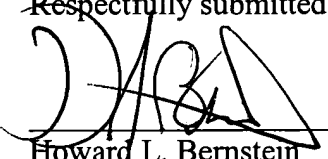
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated June 8, 2004 in which the Examiner has required a restriction, Applicant responds by electing Group II, *i.e.*, claims 24-26, drawn to a semiconductor device. This election is made without prejudice to the filing of a divisional application directed to the non-elected claims.

Prompt and favorable action on the elected claims is now respectfully requested.

Respectfully submitted,

  
Howard L. Bernstein  
Registration No. 25,665

SUGHRUE MION, PLLC  
Telephone: (202) 293-7060  
Facsimile: (202) 293-7860

WASHINGTON OFFICE  
**23373**  
CUSTOMER NUMBER

Date: July 7, 2004